



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No. : 09/975,105

Confirmation No. : 8788

Applicant : NEMECEK, Craig

Filed : October 10, 2001

T.C./A.U. : 2128

Examiner : PHAN, Thai

Title : Host to FPGA Interface in an In-Circuit Emulation System

Docket No. : CYPR-CD00184

AMENDMENT AND RESPONSE TO OFFICE ACTION

Mail Stop RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Office Action of July 13, 2006, Applicant respectfully requests further examination and reconsideration of the above-identified patent application in view of the arguments set forth below.